



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 6456

Yoshihiro TSUKIDATE

Art Unit: 2827

Serial No.: 10/633,535

Examiner: T. Q. Le

Filed: August 5, 2003

Docket No.: 100353-00173

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE INCLUDING A PLURALITY OF BLOCKS AND A SENSING CIRCUIT PROVIDED IN EACH OF THE BLOCKS FOR COMPARING DATA WITH A REFERENCE SIGNAL HAVING A LOAD IMPOSED THEREON (As Amended)

**RESPONSIVE AMENDMENT**

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

June 6, 2005

Sir:

This paper is filed in response to the Office Action mailed February 4, 2005, the period for responding having been extended by the attached petition for a one-month extension of time to June 4, 2005. In the prosecution of the above-captioned patent application, please consider the following: